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USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM; ☒ Burels

Default operator:
☒ Highlight all hit terms initially

	U	1	2	Document IDs	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	E
1	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4914630 A	19900403	12	Refresh arrangement in a block divided memory including a plural	365/189.04	365/189.12;		Fujishima, Kazuyasu et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4933907 A	19900612	32	Dynamic random access memory device and operation method there	365/222	365/222;		Kumanoya, Masaki et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5796669 A	19980818	24	Synchronous semiconductor memory device	365/222	365/230.03;		Araki, Takashi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6079023 A	20000620	9	Multi-bank memory devices having common standby voltage generation	713/300	711/106;		Yoon, Sei-Seung et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6226709 B1	20010501	14	Memory refresh control system	711/106	711/101;		Goodwin, Paul M. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6233192 B1	20010515	15	Semiconductor memory device	365/222	711/106;		Tanaka, Hidehiko	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6680875 B2	20040120	36	Semiconductor device, such as a synchronous DRAM, including a co	365/233	365/226;		Horiguchi, Masashi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6704234 B2	20040309	27	Semiconductor device, refreshing method thereof, memory system, a	365/222	365/230.03;		Mizugaki, Koichi	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>